Using the TFT proto board with the LV24-33 development board and Visual TFT

- 1. Solder a header to the TFT proto board (male or female). You can desolder one from an obsolete PC card if you are stuck.
- 2. Obtain a blank 3"x4" prototype or perf board. Ideally, get something with plated through holes and solder mask to make things easy to solder.
- 3. Look at the schematic for the MikroMMB_for_PIC24. The goal is to wire up the perf board so that you can cable it to the ports on the LV24-33 board using ribbon connectors. The connections required are shown below, along with the capacitors needed for the touch controller. The advantage of doing this is that the preset for the MikroMMB_for_PIC24 board can be used in Visual TFT.
- 4. Look carefully at the LV24-33 board and disconnect any peripheral that is using the same ports. Turn off the monitoring LED's on port B if you want the touch pad to work. The other port LED's don't seem to be a problem to the operation of the TFT.



The picture below shows the proto board connected to a Parallax 3"x4" proto board. On the back of the proto board are five 10 pin male headers just like the ones on the LV24-33 board. This allows the use of straight-through 10 conductor ribbon connectors which is a lot neater than a rat's nest of single wires. I ran out of ribbon cables and used a few wires with female Molex connectors soldered to both ends.



For power, you can use any of the VCC3 connections on the LV24-33 board. The board above has a 3.3 volt regulator and screw connector terminals so that it can be powered independently by a 5 volt or greater DC source.

Once you connect the TFT board and power it up via a USB connection (via the LV24-33), the back light should come on, however, there will be nothing on the display.

Visual TFT set up

- 1. Run Visual TFT and open a sample program for the MikroMMB_for_PIC24.
- 2. You will have to comment out AD1PCFGL = 0xCFFF; in the generated code
- 3. Change ADC threshold to 700. Otherwise the touch pad will not work.
- 4. Change the target to P24FJ96GA010 if that is your target CPU (you may have a dsPIC).

| Hardware Patterns | |
|--|---|
| Select among sets of hardware patterns w | hich include all mE Multimedia Development Boards |
| MikroMMB_for_PIC24 | Save As |
| Target Compiler | |
| Choose your compiler from the list | Show warning for externally changed files |
| mikroC PRO for dsPIC | Generate config file (CFG) on each save |
| Advanced Settings | |
| General TFT Touch Panel | Resources |
| Device Settings | |
| Select your microcontroller and set clock frequency | Optionally add your MCU specific initialization code |
| Target Device | Init Code: |
| ₽24FJ96GA010 • | void Init_MCU() { |
| Device Clock [Hz] | } |
| 32000000 | _ |
| | |
| Display Settings | |
| Select your display controller and size | |
| Display Controller | Display Size |
| HY8347-D | 320×240 |
| inder b | |
| | |
| | |
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| Hardware Patterns | | | | |
|---|--|---|--|-----------------------|
| Select among sets of hard | ware patterns | which include | all mE Multimedia Dev | elopment Boards |
| MikroMMB_for_ | PIC24 | | - | Save As |
| Target Compiler | | | | |
| Choose your compiler from | n the list | | ✓ Show warning for ext | ernally changed files |
| mikroC PRO for dsPIC | | • | 🗸 Generate config file (| CFG) on each save |
| Advanced Settings — | | | | |
| General TFT | Touch Panel | Resources | | |
| This section a Control and I TFT_DataPort | allows you to man Data line connectio | ually set each co ons vary depend TFT_Dat | onnection line of your displa ding on your target hardwa a Port_Direction | ay. Wre. |
| TFT_RST LAT | C1_bit | TFI | _RST_Direction TRISC | 1_bit |
| TFT_BLED LAT | D2_bit | TFT | _BLED_Direction TRISD: | 2_bit |
| TFT_RS LAT | B15_bit | TI TI | T_RS_Direction TRISB | 15_bit |
| | D5 bit | т | T RD Direction TRISD | 5 bit |
| TFT_WR LAT | D4_bit | TF | T_WR_Direction TRISD | 4_bit |
| | | | | |

Comment out the AD1PCFGL reference here or change it in the generated code.

| Project Settings | | X | | | |
|--|--------------------------------|------------------------------|--|--|--|
| | | | | | |
| Hardware Patterns | | | | | |
| Select among sets of hardware patterns v | vhich include all mE Multimedi | a Development Boards | | | |
| MikroMMB_for_PIC24 | | Save As | | | |
| Target Compiler | | | | | |
| Choose your compiler from the list | 🗸 Show warning l | for externally changed files | | | |
| mikroC PRO for dsPIC | 🔹 🔍 🗸 Generate confi | g file (CFG) on each save | | | |
| Advanced Settings | | | | | |
| General TFT Touch Panel | Resources | | | | |
| | | | | | |
| A/D Settings | Connections | | | | |
| READ-X: A/D Channel 13 | DriveX_Left | LATB13_bit | | | |
| READ-Y: A/D Channel 12 | DriveX Right | LATB11 bit | | | |
| Init Code: | Drive¥ IIn | | | | |
| void Init_ADC() { | Drivel Down | | | | |
| Delay_ms(150); ADC1_Init(); | | | | | |
| } | DriveX_Left_Direction | IRISBI3_bit | | | |
| | DriveX_Right_Direction | TRISB11_bit | | | |
| | DriveY_Up_Direction | TRISB12_bit | | | |
| ADC Threshold Value 700 | DriveY_Down_Direction | TRISB10_bit | | | |
| Calibration | | | | | |
| Manual O Preset | | | | | |
| XMin 102 XI | Max 860 YMin 95 | ҮМаж 872 | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| OK | Cancel | | | | |

| Hardware Patterns | | | | |
|--|---|--|--|--|
| Select among sets of hardware patterns which | n include all mE Multimedia Development Boards | | | |
| MikroMMB_for_PIC24 | Save As | | | |
| Target Compiler | | | | |
| Choose your compiler from the list 🗸 Show warning for externally change | | | | |
| mikroC PRO for dsPIC Generate config file (CFG) on each save | | | | |
| Advanced Settings | | | | |
| General TFT Touch Panel Re | sources | | | |
| Global Declarations: | Options: V JPEG as BMP V Use mE file system Get Data Code: | | | |
| // MMC/SD Connections sbit Mmc_Chip_Select at LATG9_bit; sbit Mmc_Chip_Select_Direction at TRISG9 // end of MMC/SD // TFT Get Data globals | <pre>char* TFT_Get_Data(unsigned long offset, unsigned long start_sector; unsigned int pos; start_sector = Mmc_Get_File_Write_Sectc pos = (unsigned long)offset%512;</pre> | | | |
| Init Code: void Init_Ext_Mem() { // Initialize SPI PPS_Mapping(19, _OUTPUT, _SDO2); PPS_Mapping(21, _OUTPUT, _SCK2OUT), PPS_Mapping(26, _INPUT, _SDI2); | <pre>if(start_sector == currentSector+1) { Mmc_Multi_Read_Sector(Ext_Data_Buffe currentSector = start_sector; } else if (start_sector != currentSector) { vector = start_sector file name in code</pre> | | | |
| | | | | |

Success.



If there are any errors in this document, leave a message at <u>www.nlcpr.com</u> and I will fix it. I'd really hate to burn up anyone's TFT or development board.